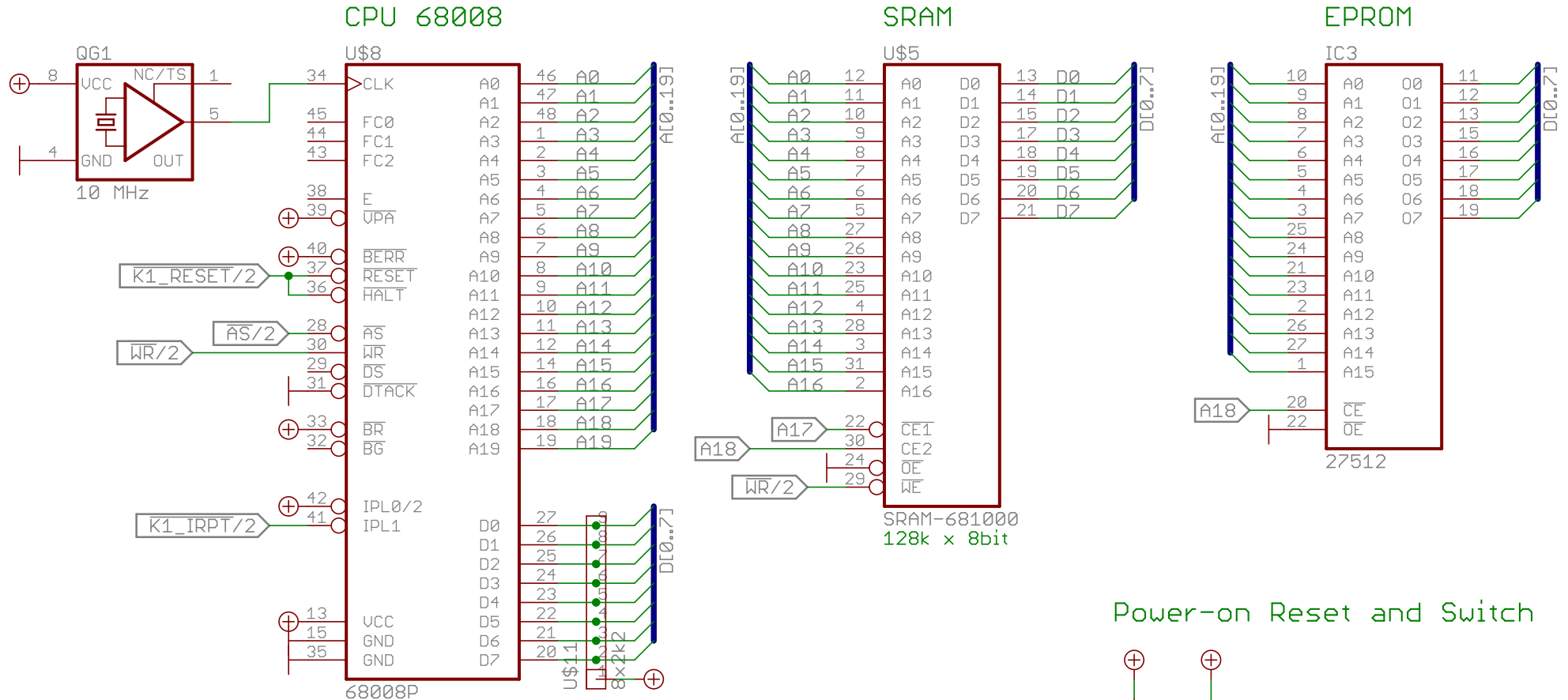


# Super-Sinclair K1-Bus Microcomputer 68008 Processor board

CC-BY-SA kio@little-bat.de Version 0.2ss (Super-Sinclair) 2014-01-16



## Memory and I/O Enables

3 address ranges are decoded: RAM, ROM and fast I/O

%110 - max. 128k of SRAM  
%101 - max. 128k of EPROM  
%011 - K1 bus I/O for fast devices

%000 - ROM (mirror)  
%001 - ROM (mirror)  
%100 - ROM (mirror)

%010 - bus collision between RAM and I/O  
%111 - interrupt acknowledge

## Power-on Reset and Switch

