S16MD01/S16MD02 S26MD01/S26MD02

Features

- 1. Compact 8-pin dual-in-line package type
- 2. RMS ON-state current I_T : 0.6Arms
- 3. Built-in zero-cross circuit (S16MD02/S26MD02)
- 4. High repetitive peak OFF-state voltage
 S16MD01 / S16MD02 V_{DRM}: MIN. 400V
 S26MD01 / S26MD02 V_{DRM}: MIN. 600V
- 5. Isolation voltage between input and output ($V_{\rm iso}$: 4,000Vrms)
- 6. Recognized by UL, file No. E94758
- 7. Approved by CSA No. LR63705

Applications

- 1. Oil fan heaters
- 2. Microwave ovens
- 3. Refrigerators

■ Model Line-ups

	For 100V lines	For 200V lines
No built-in zero- cross circuit	S16MD01	S26MD01
Built-in zero- cross circuit	S16MD02	S26MD02

Absolute Maximum Ratings

Parameter			Symbol	Rating	Unit	
Input	Forward current	IF	50	mA		
	Reverse voltage	VR	6	V		
Output	RMS ON-state currer	I _T 0.6		A rms		
	*1Peak one cycle surge current		I surge	6	А	
	Repetitive peak OFF- state voltage	S16MD01 / S16MD02	N	400	V	
		S26MD01 / S26MD02	V DRM	600	V	
*2 Isolation voltage			V iso	4 000	V rms	
Operating temperature		T opr	- 25 to + 80	°C		
Storage temperature		T stg	- 40 to + 125	°C		
*3Soldering temperature		T sol	260	°C		

*1 50Hz sine wave

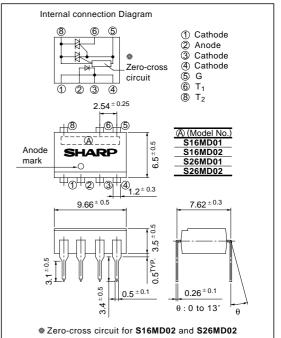
*2 AC for 1 minute, 40 to 60% RH, f = 60Hz

*3 For 10 seconds

8-Pin DIP Type SSR for Low Power Control

Outline Dimensions

(Unit: mm)



Terminal (), (3) and (4) are common ones of cathode. To radiate the heat, solder all of the lead pins on the pattern of PWB.

(Ta = 25 °C)

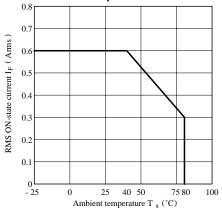
" In the absence of confirmation by device specification sheets, SHARP takes no responsibility for any defects that occur in equipment using any of SHARP's devices, shown in catalogs, data books, etc. Contact SHARP in order to obtain the latest version of the device specification sheets before using any SHARP's device."

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

Parameter			Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input	Forward voltage		V _F	$I_F = 20 m A$	-	1.2	1.4	V
	Reverse current		IR	$V_R = 3V$	-	-	10	μΑ
Output	Repetitive peak OFF-state current		I drm	$V_{DRM} = Rated$	-	-	100	μA
	ON-state voltage		V _T	$I_T = 0.6A$	-	-	3.0	V
	Holding current		I _H	$V_{\rm D} = 6V$	-	-	25	mA
	Critical rate of rise of OFF-state voltage		dV/dt	$V_{DRM} = (1/\sqrt{2}) \bullet$ Rated	100	-	-	$V/\mu \ s$
	Zero-cross voltage	S16MD02 S26MD02	Vox	Resistance load $I_F = 15mA$	-	-	35	v
Transfer charac- teristics	Minimum trigger current		I _{FT}	$V_D = 6V, R_L = 100 \Omega$	-	-	10	mA
	Isolation resistance		R ISO	DC500V, 40 to 60 % RH	5 x 10 ¹⁰	1011	-	Ω
	S16MD01 S26MD01 S16MD02 S26MD02		$V_D = 6V, R_L = 100 \Omega$	-	-	100	μs	
			t on	$I_F = 20 m A$	-	-	50	μs







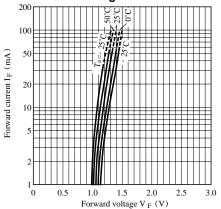


Fig. 2 Forward Current vs. Ambient Temperature

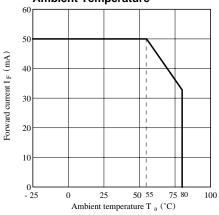
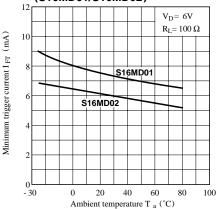
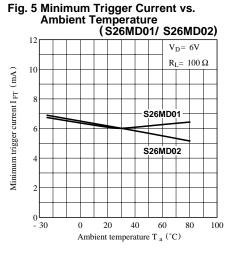


Fig. 4 Minimum Trigger Current vs. Ambient Temperature (S16MD01/S16MD02)







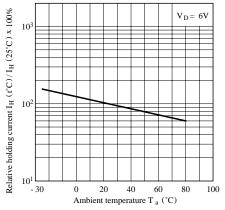


Fig. 9 Turn-on Time vs. Forward Current (S16MD01)

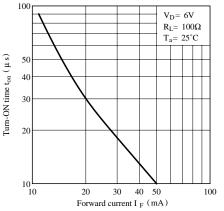


Fig. 6 ON-state Voltage vs. Ambient Temperature

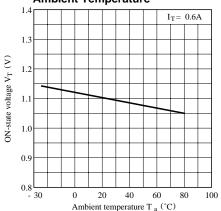


Fig. 8 ON-state Current vs. ON-state Voltage

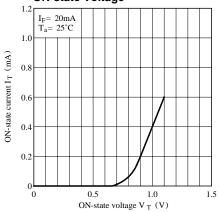
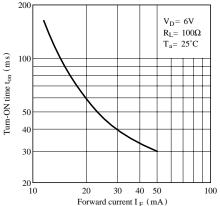
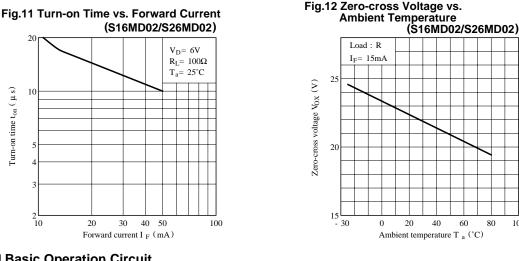


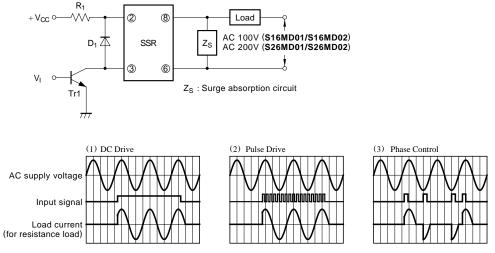
Fig.10 Turn-on Time vs. Forward Current (S26MD01)



100



Basic Operation Circuit



- Notes 1) If large amount of surge is loaded onto V CC or the driver circuit, add a diode D 1 between terminal 2 and 3 to prevent reverse bias from being applied to the infrared LED.
 - 2) Be sure to install a surge absorption circuit. An appropriate circuit must be chosen according to the load (for CR, choose its constant). This must be carefully done especially for an inductive load.
 - 3) For phase control, adjust such that the load current immediately after the input signal is applied will be more than 30mA.

Precautions for Use

- 1) All pins must be soldered since they are also used as heat sinks (heat radiation fins). In designing, consider the heat radiation from the mounted SSR.
- 2) For higher radiation efficiency that allows wider thermal margin, secure a wider round pattern for Pin No.8 when designing mounting pattern. The rounded part of Pin No.5 (gate) must be as small as possible. Pulling the gate pattern around increases the change of being affected by external noise.
- 3) As for other general cautions, refer to the chapter"Precautions for Use"

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Datasheets for electronics components.