



JTAG

TCK<CCLK>	1	2	GND=0V
TDO<D/P>	3	4	VCC=+5V
TMS</PROG>	5	6	
UCC=+5V	7	8	
TDI<DIN>	9	10	GND=0V

SU4

Pin1=red

Pin assignment JTAG-Programmer for Xilinx CLPD & FPGA		XEYON-ES	
		Pinbelegung-JTAG	
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