

DM74LS170 4 x 4 Register File with Open-Collector Outputs

General Description

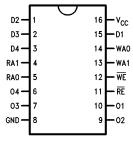
The 'LS170 contains 16 high speed, low power, transparent D-type latches arranged as four words of four bits each, to function as a 4 \times 4 register file. Separate read and write inputs, both address and enable, allow simultaneous read and write operation. Open-collector outputs make it possible to connect up to 128 outputs in a wired-AND configuration to increase the word capacity up to 512 words. Any number of these devices can be operated in parallel to generate an n-bit length. The '670 provides a similar function to this device but it features TRI-STATE® outputs.

Features

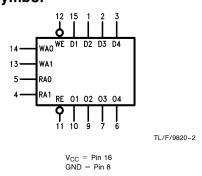
- Simultaneous read/write operation
- Expandable to 512 words of n-bits
- Typical access time of 20 ns
- Low leakage open-collector outputs for expansion

Connection Diagram

Dual-In-Line Package



Logic Symbol



Order Number DM74LS170WM or DM74LS170N See NS Package Number M16B or N16E

Pin Names	Description
D1-D4	Data Inputs
WA0-WA1	Write Address Inputs
WE	Write Enable Input (Active LOW)
RA0, RA1	Read Address Inputs
RE	Read Enable Input (Active LOW)
01-04	Data Outputs

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Absolute Maximum Ratings (Note)

Supply Voltage 7V
Input Voltage 7V
Operating Free Air Temperature Range
DM74 0°C to +70°C

Storage Temperature Range -65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter		DM74LS170		
Symbol	Farameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			V
V _{IL}	Low Level Input Voltage			0.8	V
V _{OH}	High Level Output Voltage			5.5	V
l _{OL}	Low Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C
t _s	Setup Time HIGH or LOW Dn to Rising WE	10			ns
t _h	Hold Time HIGH or LOW Dn to Rising WE	5.0			ns
t _s	Setup Time HIGH or LOW WAn to Falling WE	10			ns
t _h	Hold Time HIGH or LOW WAn to Rising WE	5.0			ns
t _w (L)	WE or RE Pulse Width LOW	25			ns

Electrical Characteristics

Over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Uints
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$				-1.5	٧
I _{CEX}	High Level Output Current	$V_{CC} = Min, V_O = 5.5V$				20	μΑ
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max,$ $V_{IH} = Min$	DM74		0.35	0.5	٧
		I _{OL} = 4 mA, V _{CC} = Min	DM74		0.25	0.4	
-I _I	Input Current @ Max	$V_{CC} = Max, V_I = 7V$	Dns, RAO, WA0			0.1	mA
	Input Voltage		WE, RE			0.2	11173
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$	Inputs			20	μΑ
			RE, WE			40	μπ
IIL	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$	RE, WE	-0.06		-0.8	
			RA1, WA1	-0.05		-0.4	mA
			DATA, RA0, WA0	-0.03		-0.4	
I _{OS}	Short Circuit Output Current	V _{CC} = Max (Note 2)	DM74	-20		-100	mA
I _{CC}	Supply Current	$V_{CC} = Max, Dn, \overline{WE},$ $\overline{RE} = 4.5V, WAn, RAn = GND$				40	mA

Note 1: All typicals are at $V_{\mbox{CC}}=5\mbox{V, T}_{\mbox{A}}=25\mbox{°C}.$

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Switching Characteristics $V_{CC} = +5.0V, T_A = +25^{\circ}C$

Symbol	Parameter	Conditions	R _L = 2k, (Units	
Cymbol			Min	Max	Oillis
t _{PLH} t _{PHL}	Propagation Delay* RA0 or RA1 to On			35 35	ns
t _{PLH} t _{PHL}	Propagation Delay RE to On			30 30	ns
t _{PLH} t _{PHL}	Propagation Delay WE to On			35 35	ns
t _{PLH} t _{PHL}	Propagation Delay Dn to On			35 35	ns

^{*}Measured at least 25 ns after entry of new data at selected location.

Switching Waveforms

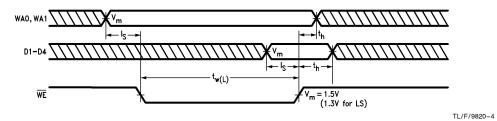


FIGURE a

Write Function Table

Write Inputs			D Inputs to
WE	WA1	WA0	5 inputo to
L	L	L	Word 0
L	L	Н	Word 1
L	Н	L	Word 2
L	Н	Н	Word 3
Н	Χ	Χ	None (Hold)

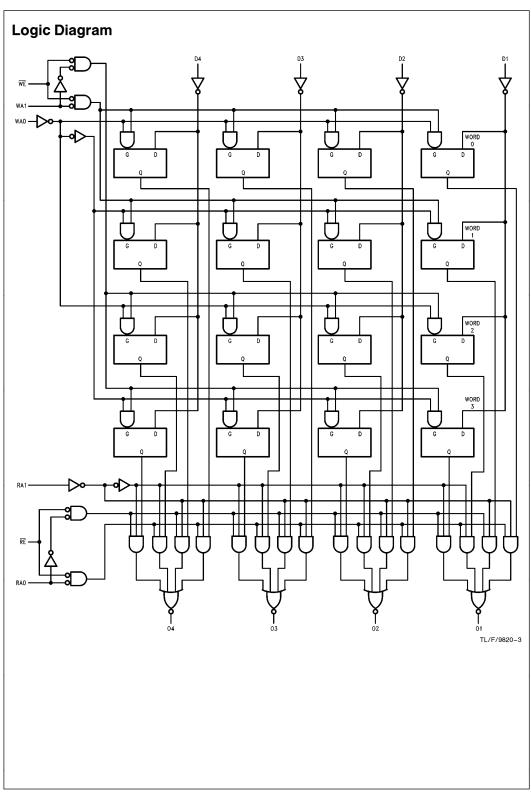
H = HIGH Voltage Level

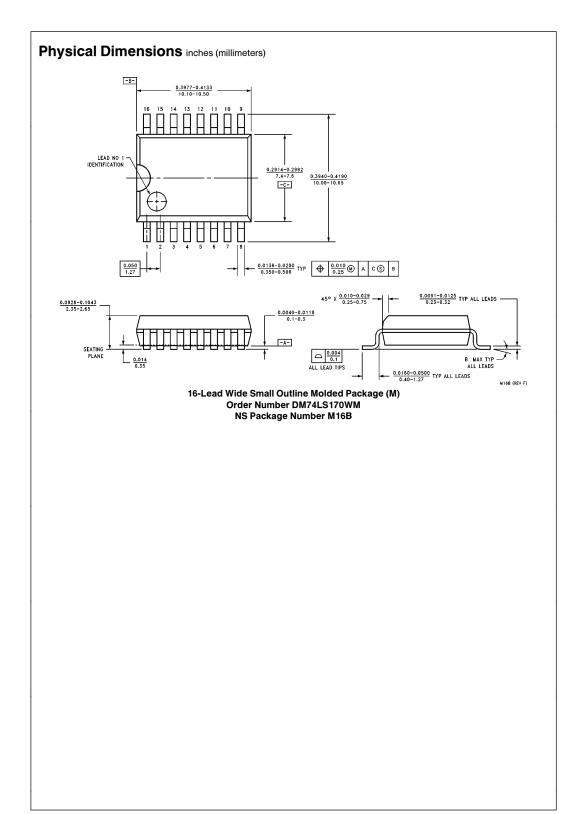
L = LOW Voltage Level

Read Function Table

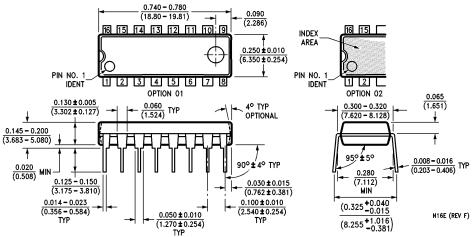
Read Inputs			Outputs from
RE	RA1	RA0	Outputo II O III
L	L	L	Word 0
L	L	Н	Word 1
L	Н	L	Word 2
L	Н	Н	Word 3
Н	Х	Х	None (High Z)

X = Immaterial





Physical Dimensions inches (millimeters) (Continued)



16-Lead Molded Dual-In-Line Package (N) Order Number DM74LS170N (N) NS Package Number N16E

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